

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Presently Amended) An apparatus comprising:

an amplitude mapping circuit for converting at least a portion of an amplitude signal to a binary value; and,

a plurality of gain control sources coupled to the amplitude mapping circuit; and,

a plurality of amplifiers coupled to at least one of the plurality of gain control sources the amplitude mapping circuit,

wherein the binary value is transmitted to at least one of the plurality of amplifiers by at least one of the plurality of gain control sources to specify a gain level of the amplifier.

2. (Original) The apparatus of claim 1, further comprising:

a rectangular to polar converter for converting a signal into amplitude and phase portions, and for transmitting said amplitude portion to the amplitude mapping circuit.

3. (Original) The apparatus of claim 2, further comprising:

a phase modulator for modulating the phase portion with a carrier signal.

4. (Previously Amended) The apparatus of claim 2, further comprising:

a modulator for modulating the phase portion with a carrier signal.

5. (Canceled).

6. (Original) The apparatus of claim 1, further comprising:

an input matching circuit coupled to the respective inputs of the plurality of amplifiers;

and

an output matching circuit coupled to the respective outputs of the plurality of amplifiers.

7. (Canceled).

8. (Presently Amended) A method for processing a signal, comprising the steps of:

separating the signal into amplitude and phase components;

generating a binary representation of at least a portion of the amplitude component; and,

applying said binary representation to at least one of a plurality of gain control sources;

specifying a gain level of one of a plurality of amplifiers in response to the generated binary representation.